



Express Mail No. **EV 346 810 935 US**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: Sebastien KERDILES et al.

Confirmation No. 8821

Application No.: 10/733,470

Group Art Unit: 3671

Filing Date: December 12, 2003

Examiner:

For: TOOLS AND METHODS FOR  
DISUNITING SEMICONDUCTOR WAFERS

Atty. Docket No.: 4717-9100

**REQUEST FOR CORRECTION OF FILING RECEIPT**

Office of Initial Patent Examination  
Customer Service Center  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

A filing receipt for the above-identified application has been received and the following correction is respectfully requested:

Please correct the address of the first inventor to read as follows: --  
**Sebastien KERDILES -Saint-Ismier, France**

Copies of the incorrect filing receipt along with a copy of the inventors' declaration are enclosed herewith in support of this change. No fee is believed to be due for this submission. Please charge any required fees to WINSTON & STRAWN LLP Deposit Account No. 50-1814.

Respectfully submitted,

Date: 5-7-04

Allan A. Fanucci (Reg. No. 30,256)

**WINSTON & STRAWN LLP**  
**CUSTOMER NO. 28765**  
**(212) 294-3311**

Enclosures

NY:862523.1



## UNITED STATES PATENT AND TRADEMARK OFFICE

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APPL NO.	FILING OR 371 (b) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/733,470	12/12/2003	3671	950	4717-9100	10	30	3

28765  
WINSTON & STRAWN  
PATENT DEPARTMENT  
1400 L STREET, N.W.  
WASHINGTON, DC 20005-3502

CONFIRMATION NO. 8821

## FILING RECEIPT



\*OC000000012142498\*

Date Mailed: 03/19/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

*Saint-Ismier, FRANCE*  
Sebastien Kerdiles, Seyssinet-Pariset, FRANCE;  
Yves-Mathieu Le Vaillant, Grenoble, FRANCE;

*Please  
Correct*

## Domestic Priority data as claimed by applicant

This appln claims benefit of 60/446,552 02/12/2003

## Foreign Applications

FRANCE 0215902 12/16/2002

If Required, Foreign Filing License Granted: 03/18/2004

Projected Publication Date: To Be Determined - pending completion of Missing Parts

Non-Publication Request: No

Early Publication Request: No

## Title

Tools and methods for disuniting semiconductor wafers

RECEIVED

MAR 22 2004

WINSTON & STRAWN LLP  
DOCKETING DEPT. (D.C.)

Preliminary Class  
173



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